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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: OHASHI, Hitoshi et al.

Group Art Unit: 1792

Serial No.: 10/047,992

Examiner: Brian K. Talbot

Filed: January 17, 2002

P.T.O. Confirmation No.: 5363

Docket: 020052

Date: June 7, 2010

For: METHOD AND APPARATUS FOR FORMING  
ELECTRIC CIRCUIT FIXING THE SEMICONDUCTOR  
DEVICE USING SUBSTRATE JIGAMENDMENT with R.C.E. and PETITION

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

An RCE is submitted herewith on even date.

This paper is in response to the Final Office Action dated February 1, 2010, extended *two months* to July 1, 2010. This paper is a Petition for an appropriate extension of time. Please charge the fees needed for such an extension of time, and any other fees which may be needed to enter this paper, to Deposit Account No. 01-2340.

The following listing of claims will replace all prior versions, and listings, of claims.

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